

FIG. 1A

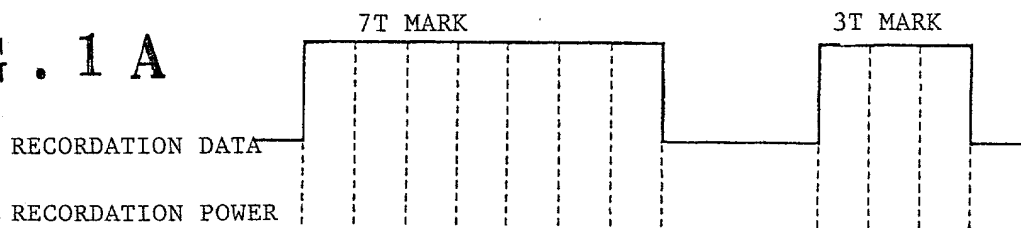


FIG. 1B

BIAS POWER

ZERO
LEVELTOP
PULSE

MULTI-PULSE

FIG. 2A

RECORDATION DATA

FIG. 2B

RECORDATION POWER

ERASURE POWER

BIAS POWER

ZERO
LEVELTOP
PULSE

MULTI-PULSE

COOL
PULSE

FIG. 3A

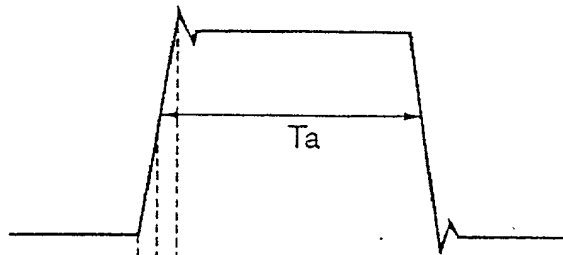


FIG. 3B

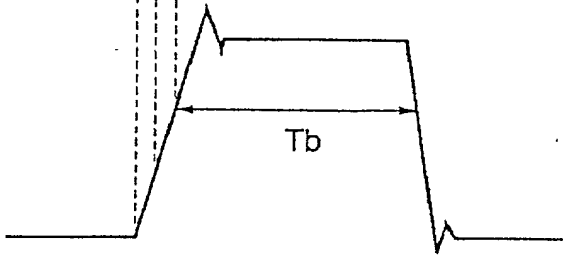


FIG. 4A

WRITE PULSE

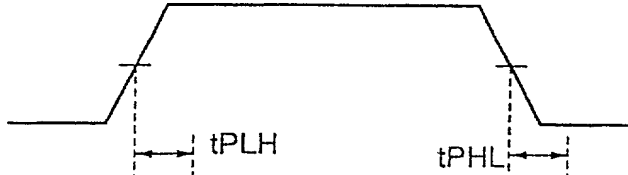


FIG. 4B

IN PHASE OUTPUT

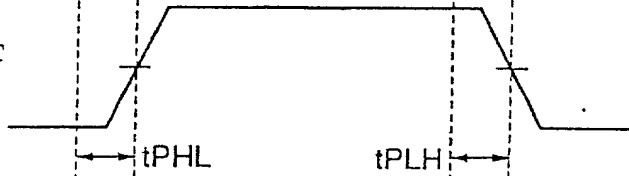
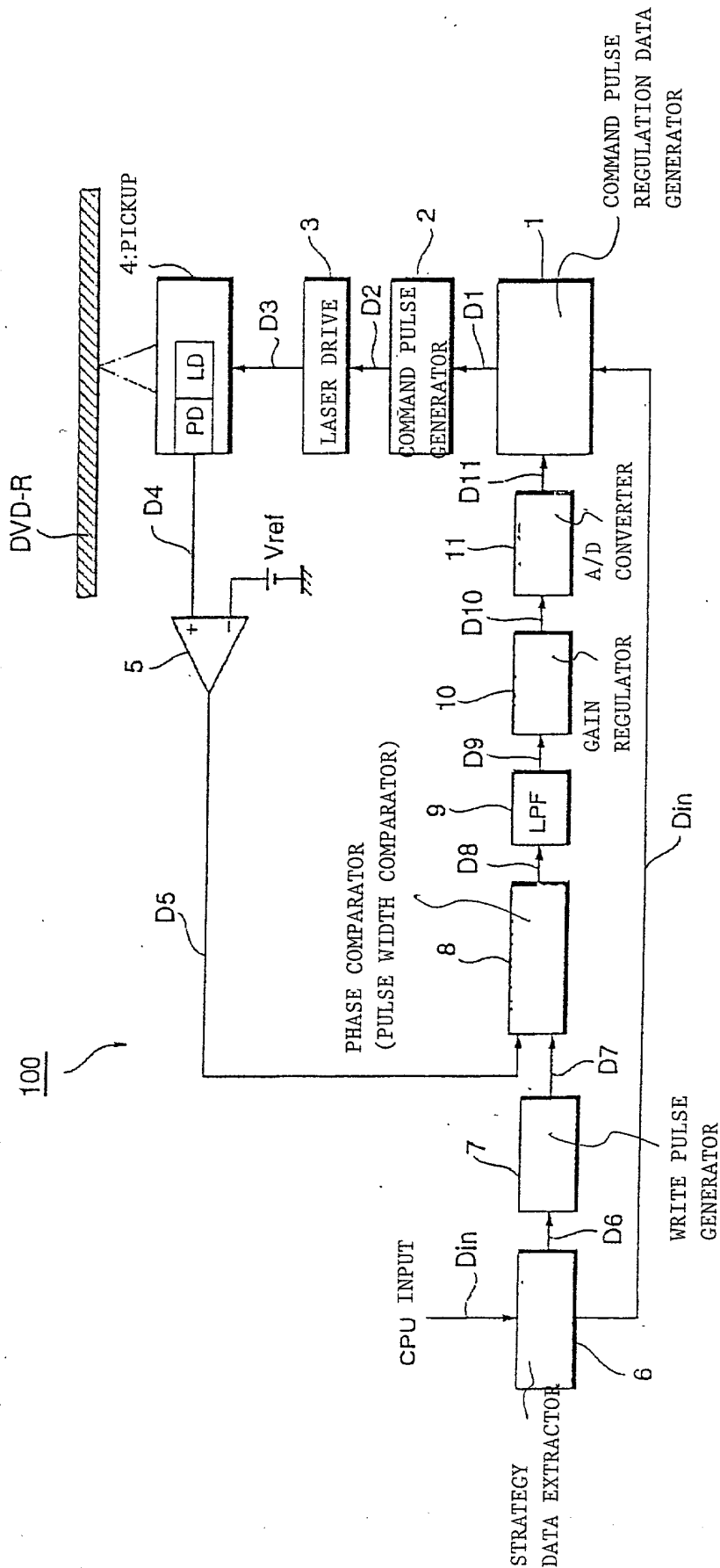


FIG. 4C

INVERTED OUTPUT



FIG. 5



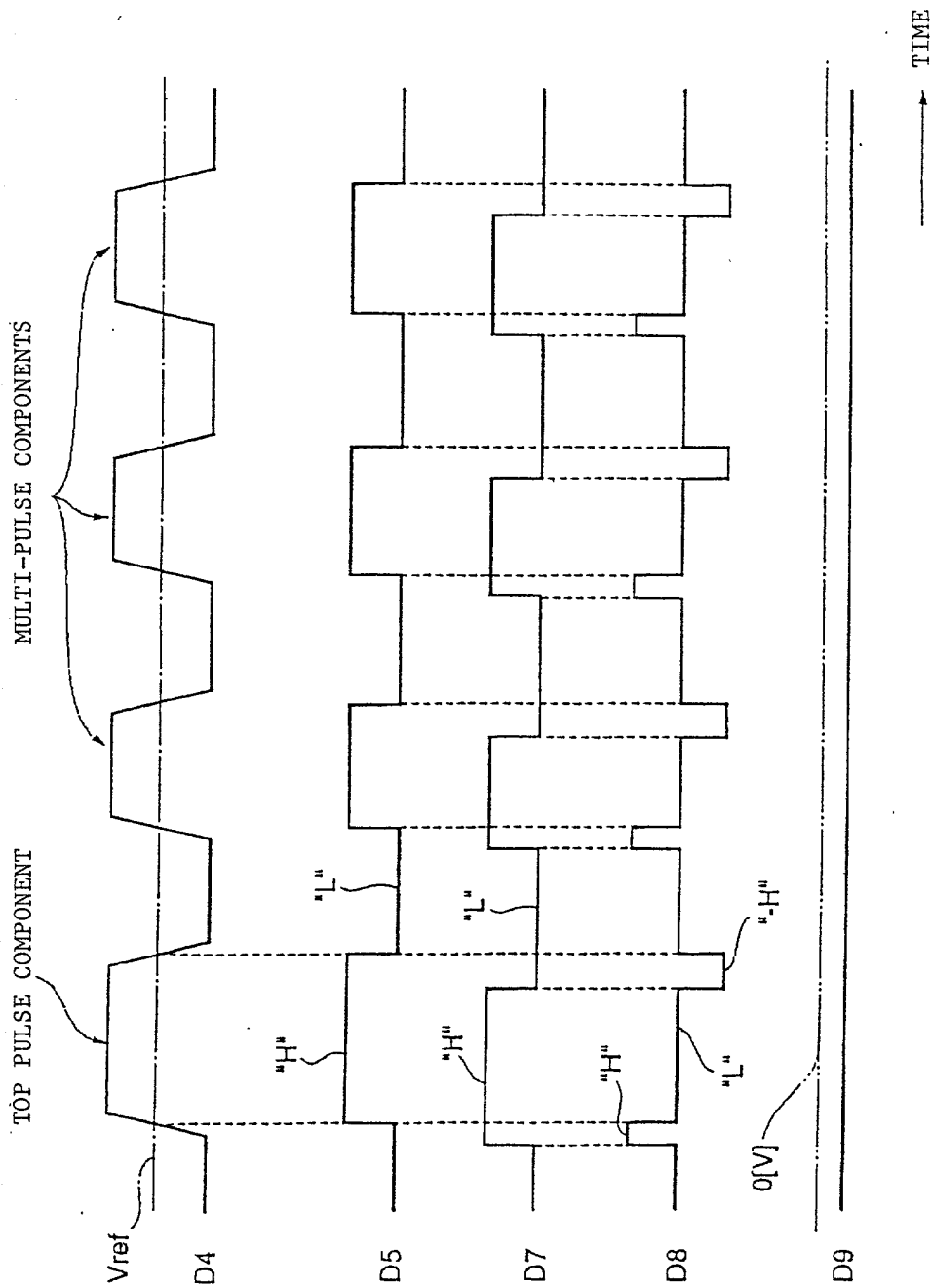


FIG. 6A

FIG. 6B

FIG. 6C

FIG. 6D

FIG. 6E

FIG. 7 A

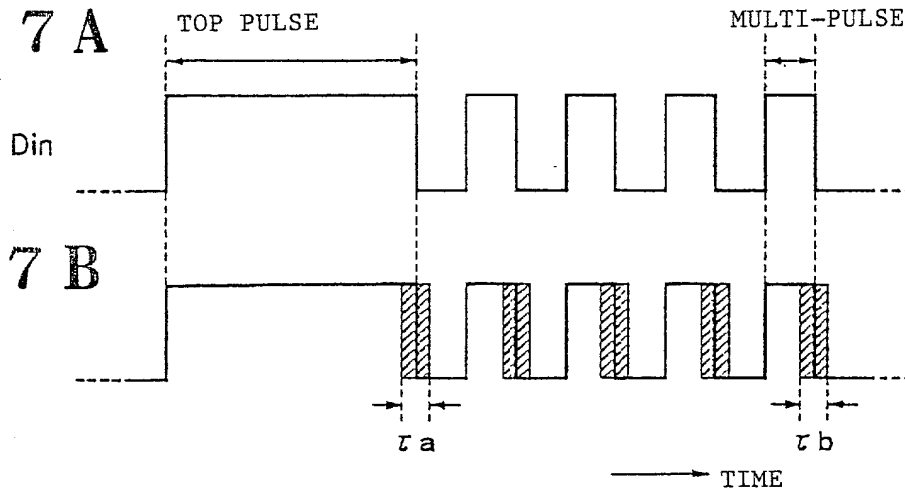
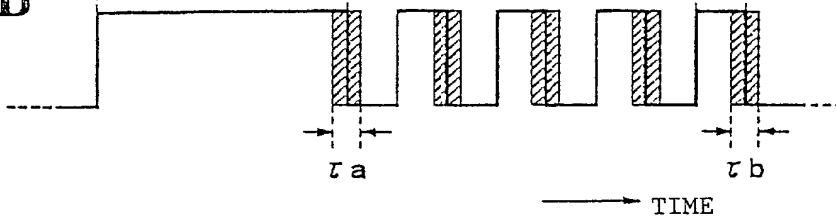
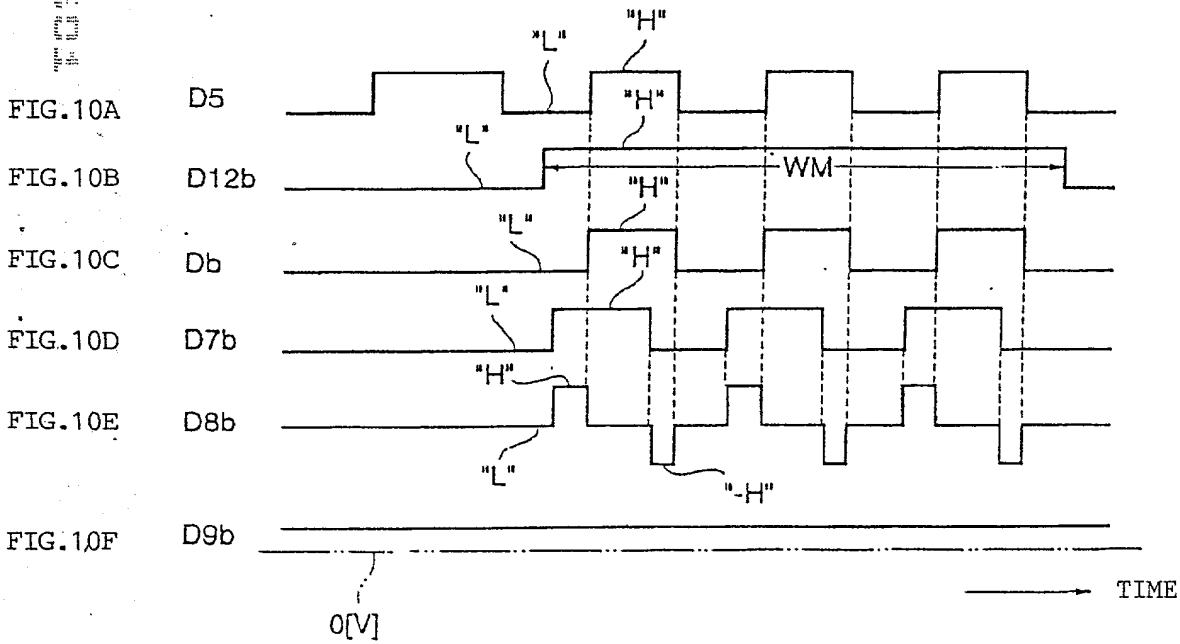
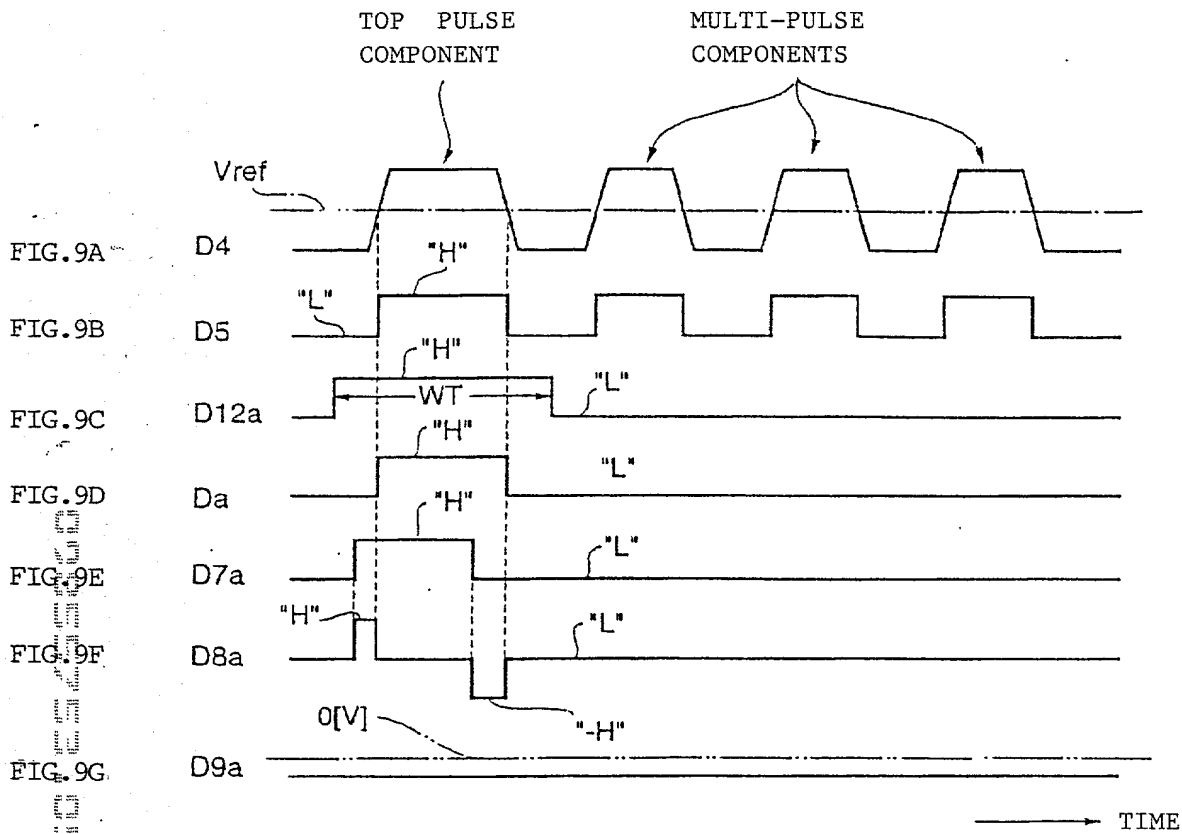
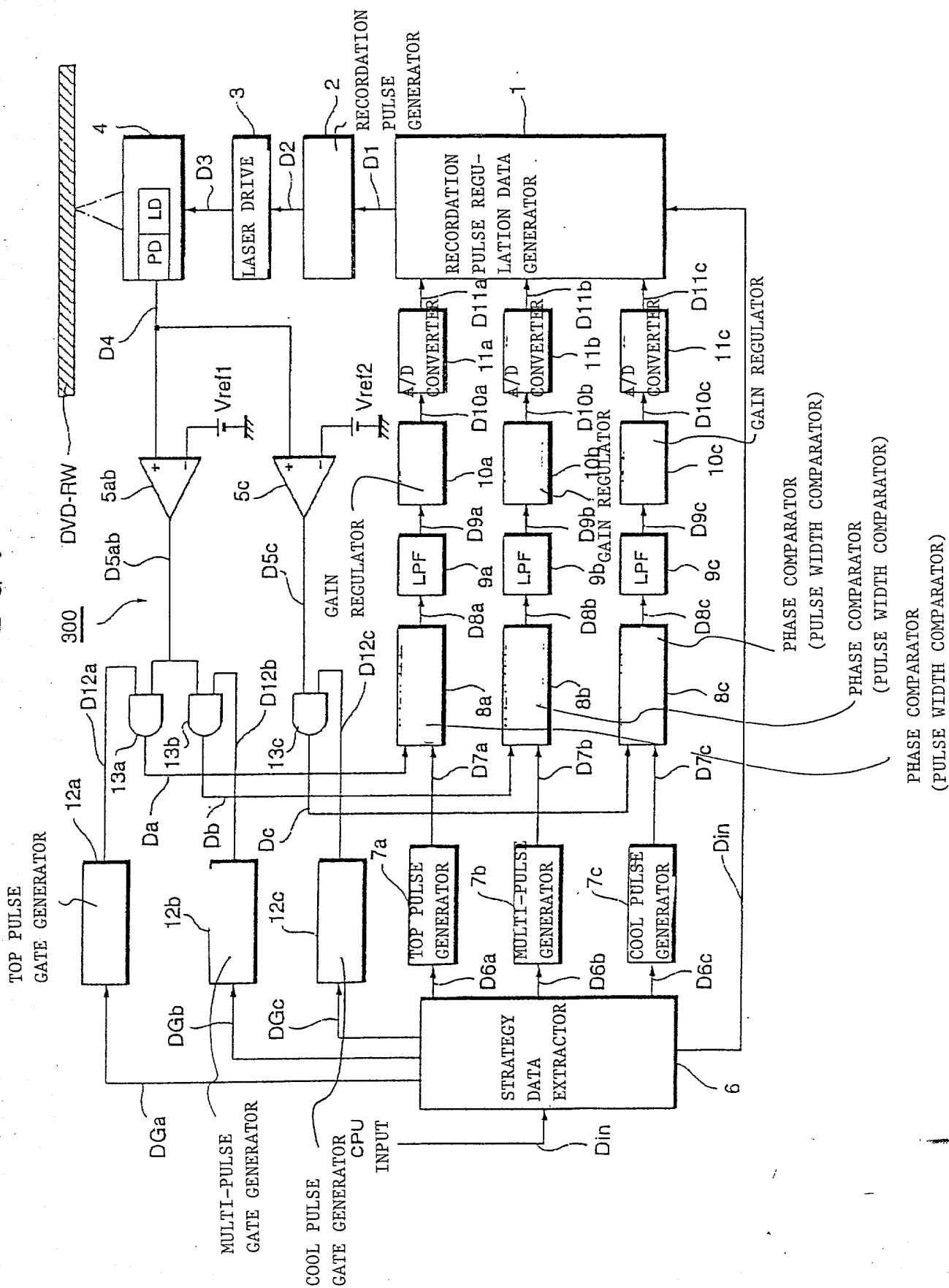


FIG. 7 B





FILED.



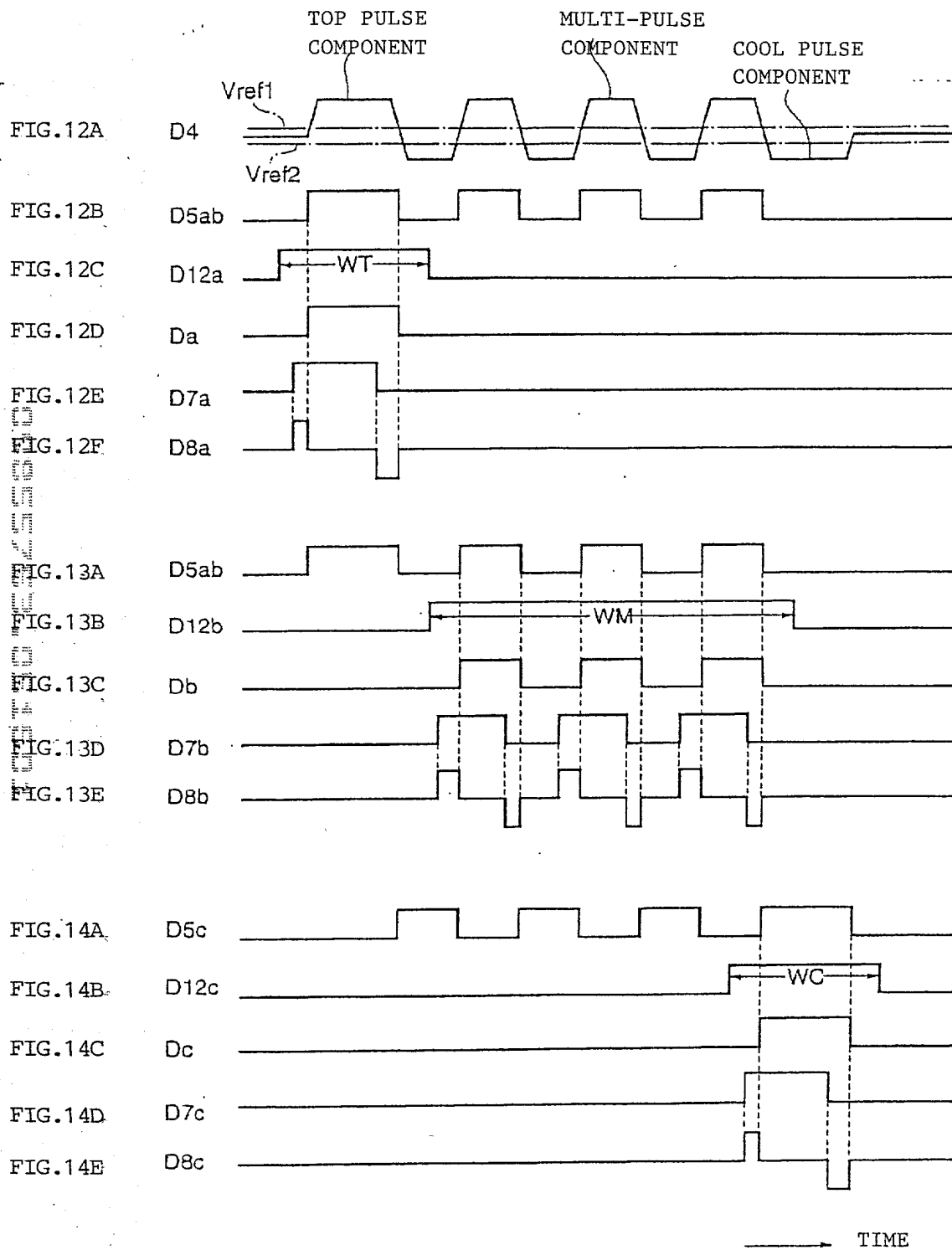


FIG. 15 A

FIG. 15 B

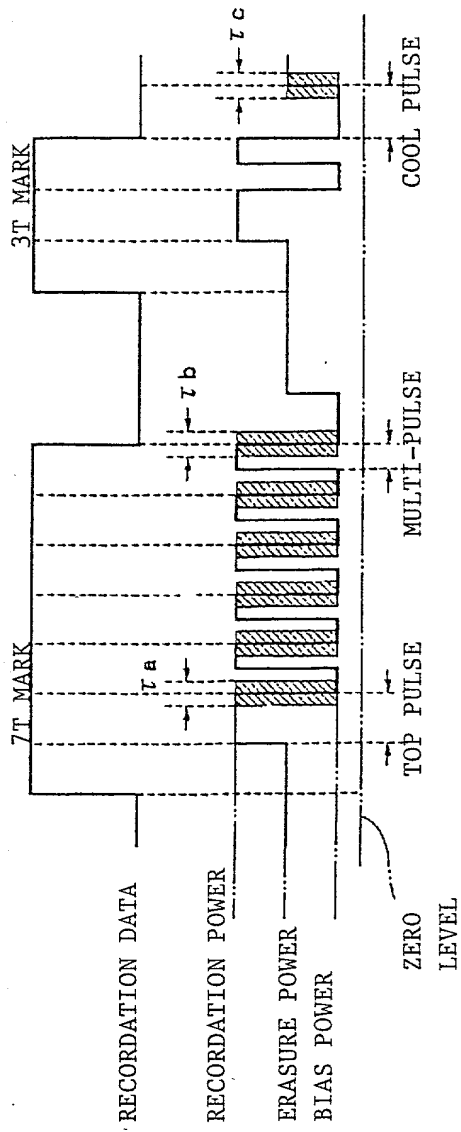




FIG. 16